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PTO/SB/21 (08-00)

TRANSMITTAL FORM (to be used for all correspondence after initial filing)	Application Number	09/916,484
	Filing Date	July 30, 2001
	First Named Inventor	Shunpei YAMAZAKI et al.
	Group Art Unit	2823
	Examiner Name	S. Foong
Total Number of Pages in This Submission	Attorney Docket Number	0756-2344

ENCLOSURES (check all that apply)		
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Firm or Individual name	Eric J. Robinson, Reg. No. 38,285 Robinson Intellectual Property Law Office, P.C. PMB 955 21010 Southbank Street Potomac Falls, VA 20165
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- 1 -

Docket No. 0756-2344

#13

Amend

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In re Patent Application of)
Shunpei YAMAZAKI et al.)
Serial No. 09/916,484)
Filed: July 30, 2001)
For: ELECTRIC DEVICE, MATRIX)
DEVICE, ELECTRO-OPTICAL DISPLAY)
DEVICE, AND SEMICONDUCTOR)
MEMORY HAVING THIN-FILM)
TRANSISTORS)

Art Unit: 2823

Examiner: S. Foong

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Rose M. Fichtel
Rose M. Fichtel

AMENDMENT

Honorable Commissioner of Patents
Washington, D.C. 20231

Sir:

In response to the Office Action dated October 24, 2002, please amend the
above-identified application as follows:

IN THE CLAIMS:

Please amend claims 3, 9, and 15 as follows:

3. (Twice Amended) A method of manufacturing a semiconductor device:
the semiconductor device comprising: at least two p-channel thin film
transistors in a pixel portion,
each of the two p-channel thin film transistors in the pixel portion fabricated
through the method comprising:
forming a semiconductor island over a substrate;
forming a gate electrode adjacent to the semiconductor island with a gate
insulating film therebetween;